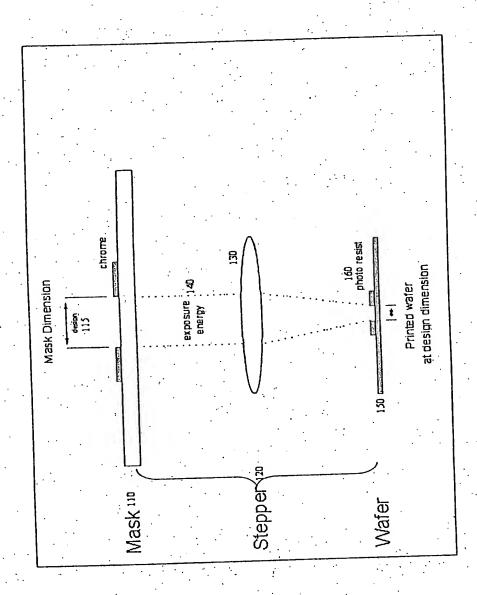
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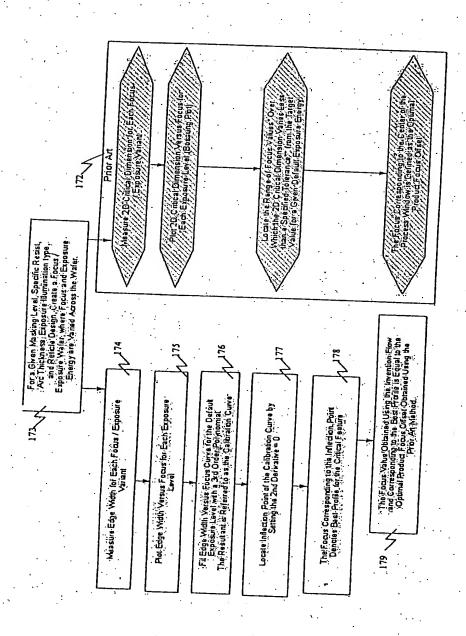


Fig. 1A

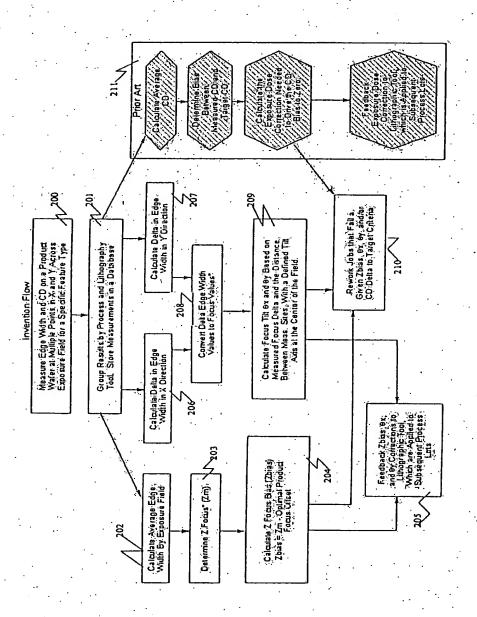


Fig. 1B

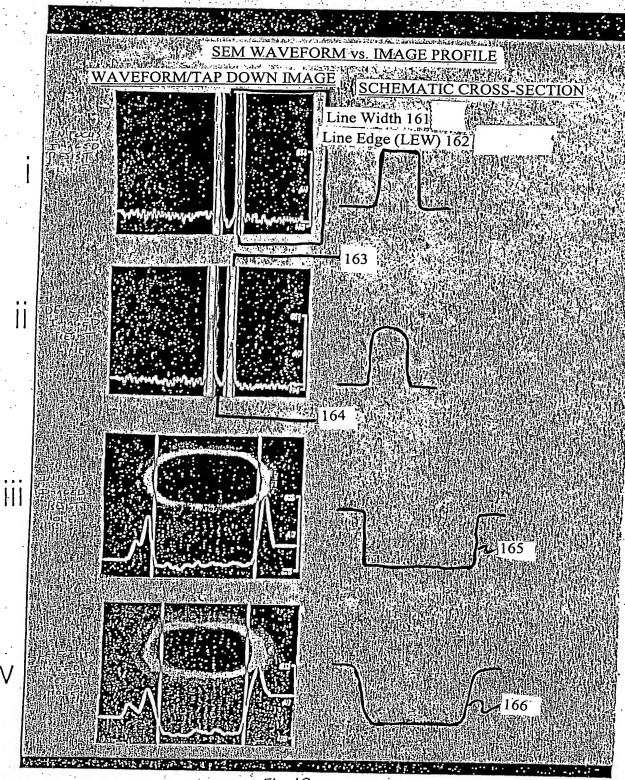


Fig. 1C

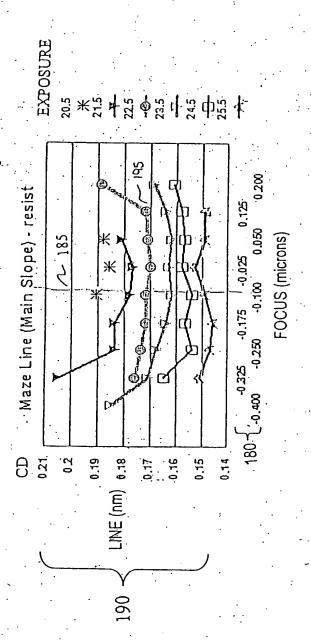


Fig. 1D

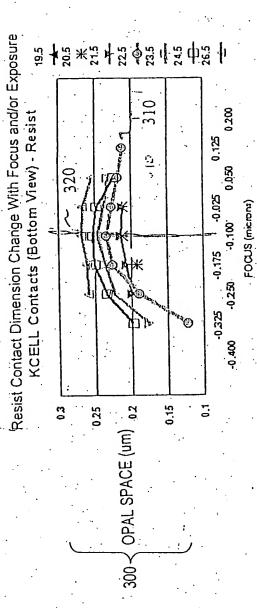


Fig. 2

Fig 3

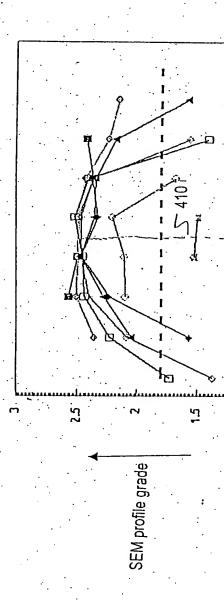


Fig. 4

0.175 0.225

0.125 0.05 0.025 0.1 Focus

0.35 0.275

Critical Dimension of Imaged Photoresist as Measured by CD 1054 180 178 176 Critical Dimension (nm) 175 179 177 174

Position in Exposure Field

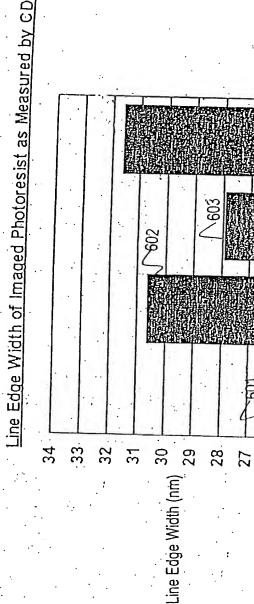
Upper Right

Upper Left

Lower Left

172

Lower Right



Upper Right Position in Exposure Field Lower Left Lower Right U

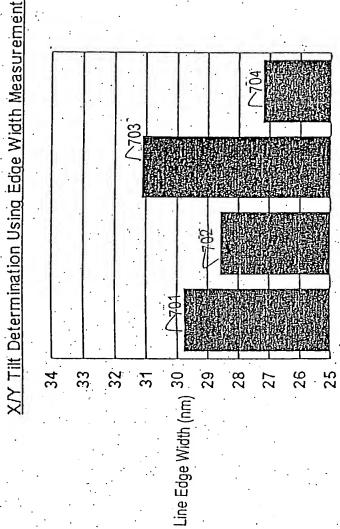
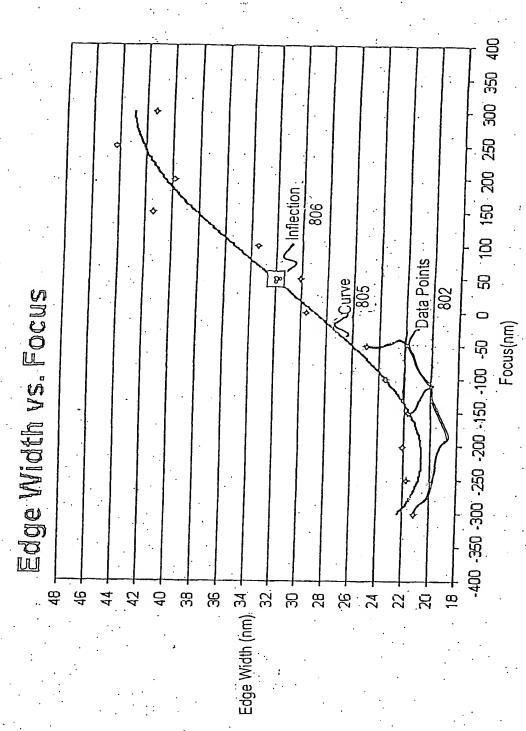


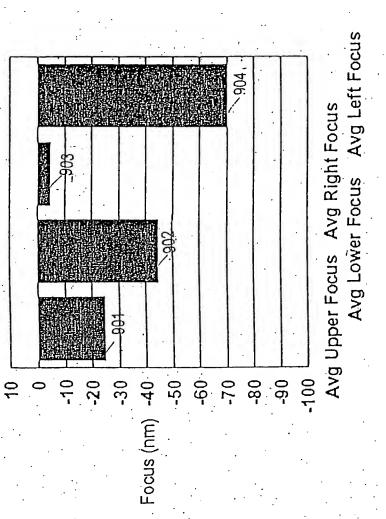
Fig. 7

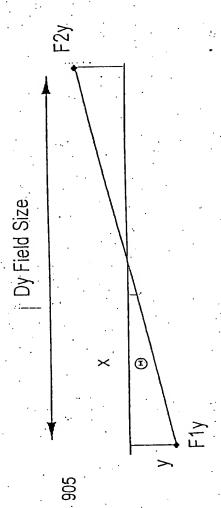
Avg Upper EW Avg Right EW Avg Lower EW Avg Left EW



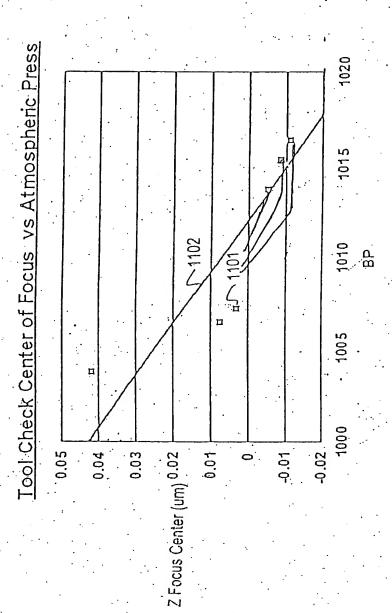
X/Y Tilt Determination Using Edge Width Measurement

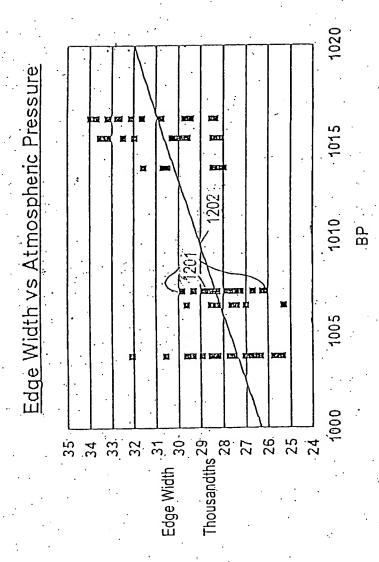
Fig. 9





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1.05 .

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TABLE OF RUN RULES

1310 Tool	1320 Technology	1330 WaferPN	1340 Process	1350 Opt	1360 Default Exposure	1370 Focus
U82V	ICE8S3D	*	МС	F	19.00	0.18
U82V	ICECBS2	*	мс	F	19.00	6.18 .
U82V	ICEC8S3	*	мс	F	19.00	8.18
U84V	CMOS6X1	*	MC	F	20.60	8.88
U86V	CS01950	*	MC9S	F.	24.00	-0.19
Vasu	CSOI9SB	8888888J8648	MC9S	F	30.00	-0.18
U86V	CS019S0	0000008J0645	MC9S	F	38.88	-8.18
V86V	CS019S0	,0000057P6438	мс9s	F	28.00	-8.05
V86V	CS019S2	*	MC9S	F	25.88	-8.10
U92V	CS019S8	*	MC9S ·	. F	24	-0.10
U92V	CS019S8 -	999998J9639	MC9S	F	30.00	-8.18
U92V	CS019S8	0000008J0640	MC9S	F	38.88	-0.10·

Fig. 13 (Prior Art)

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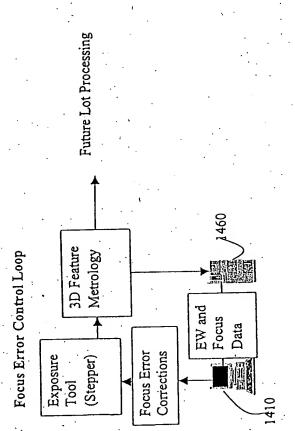


Fig. 14